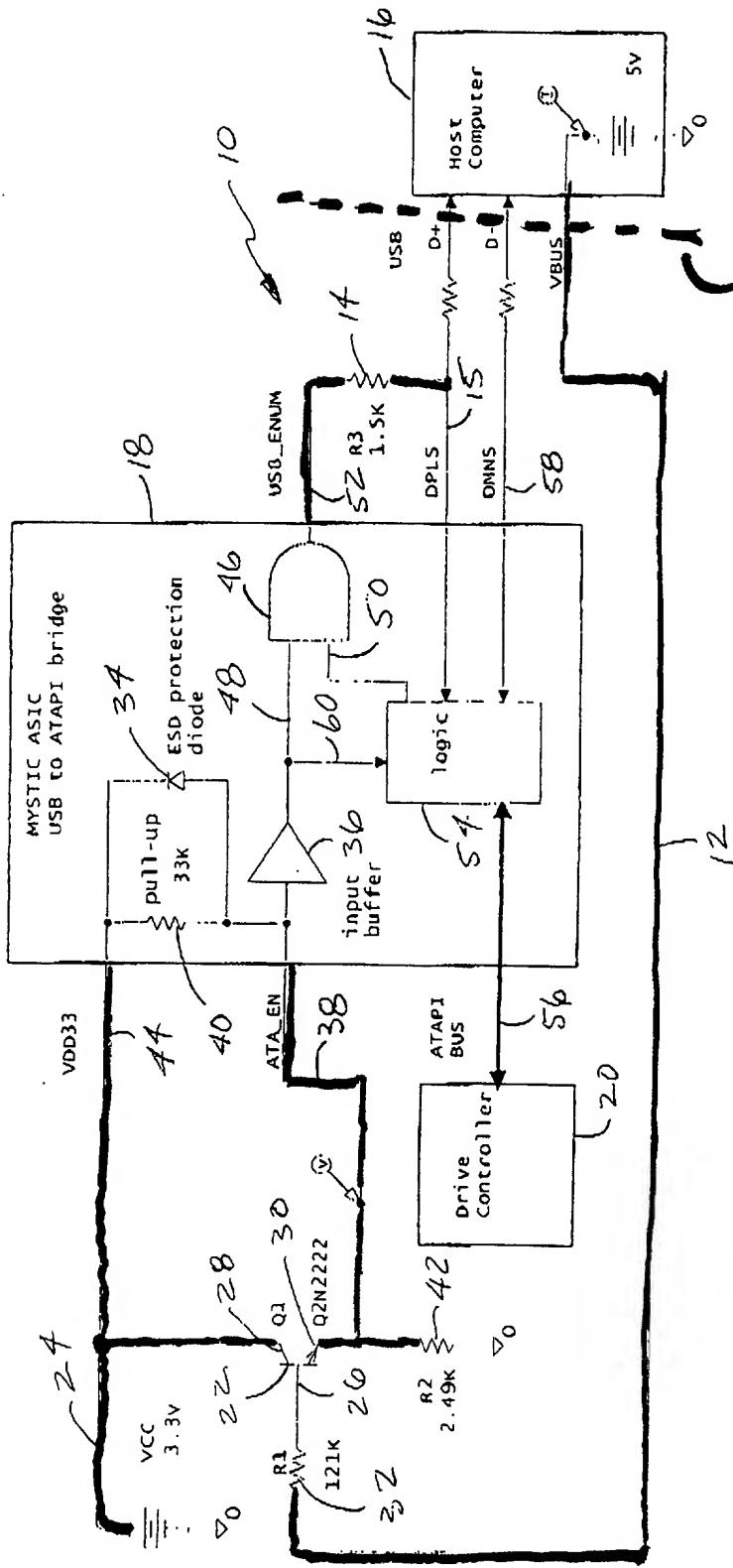


15

17

FIG. 1



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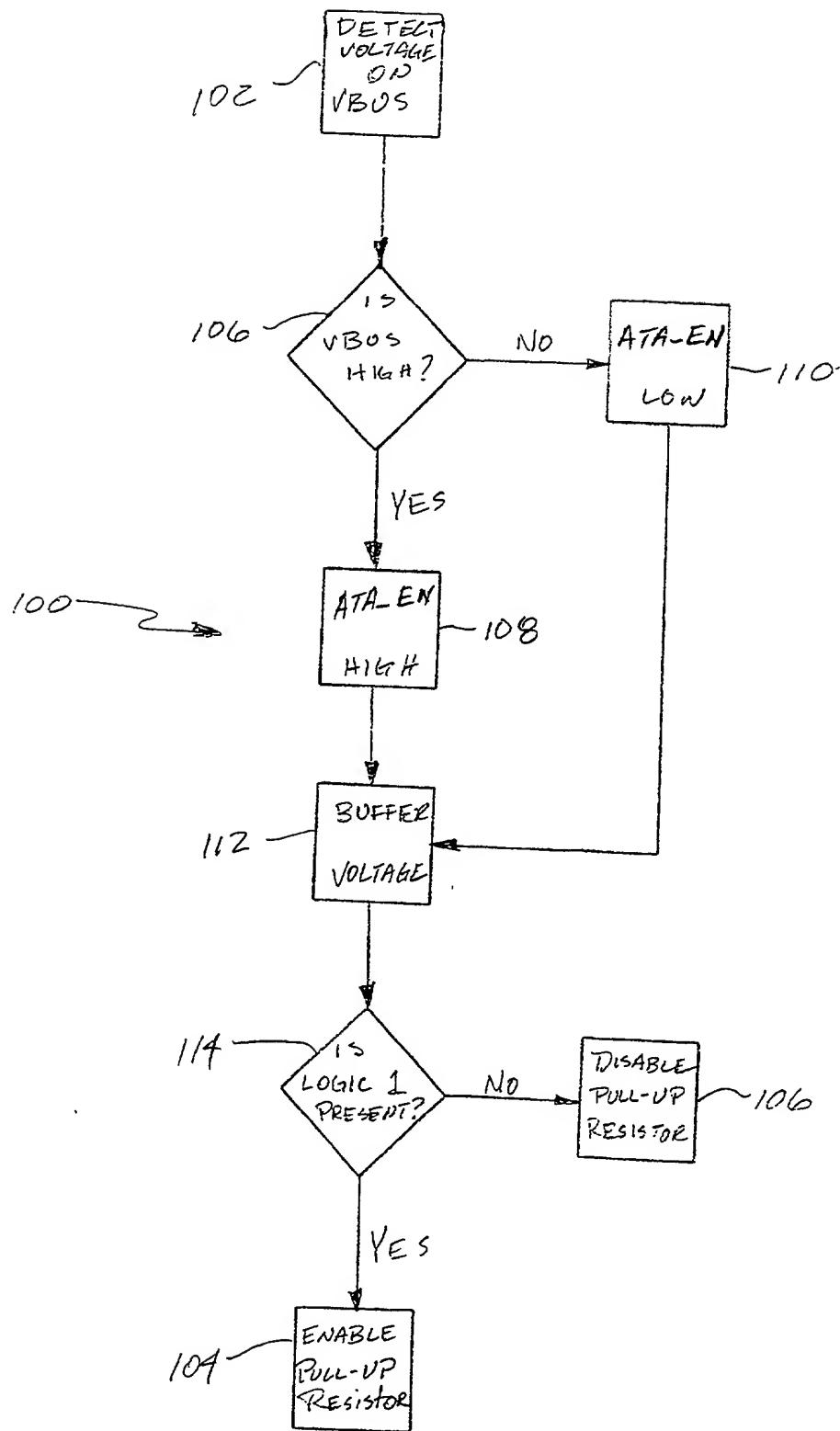
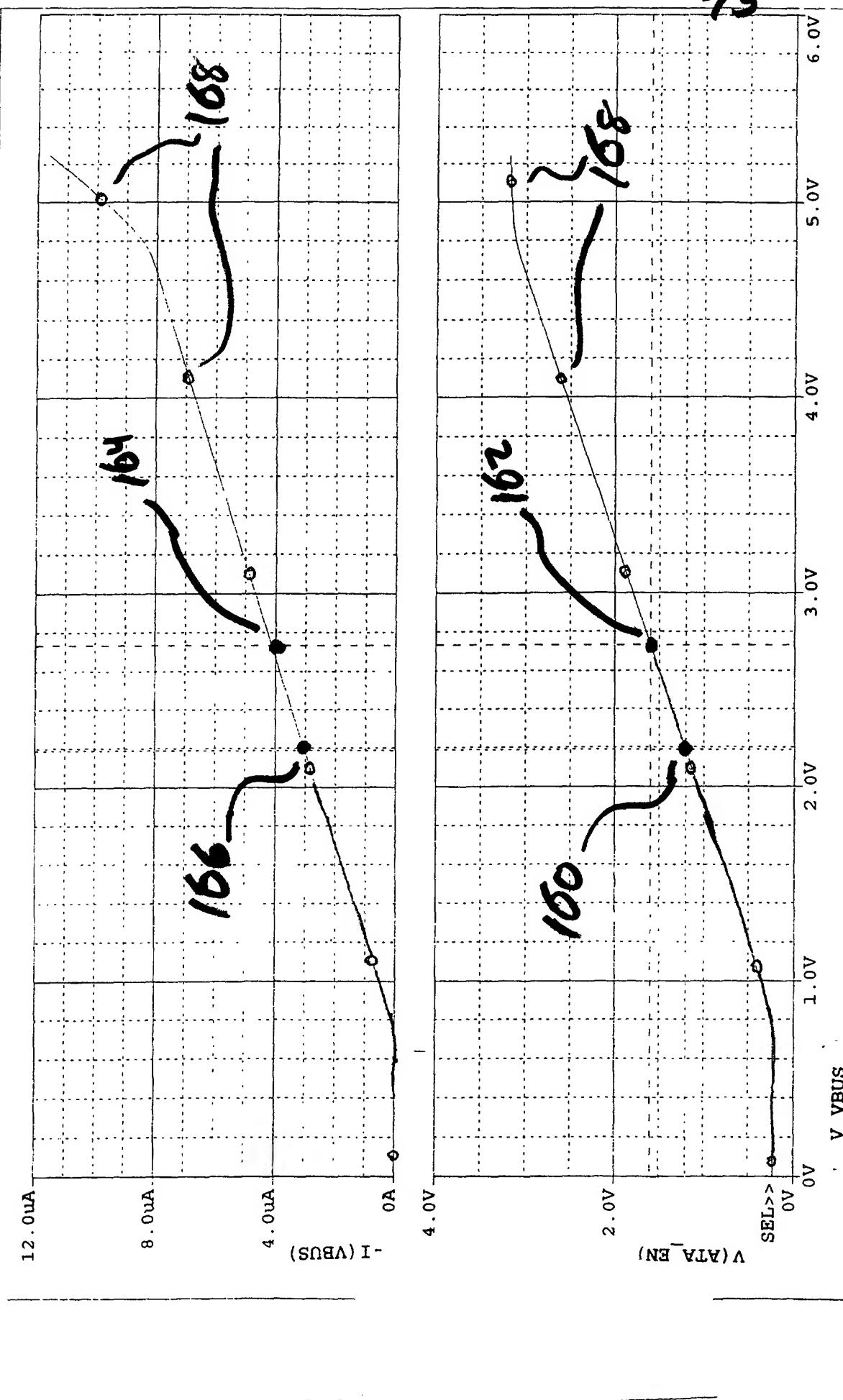
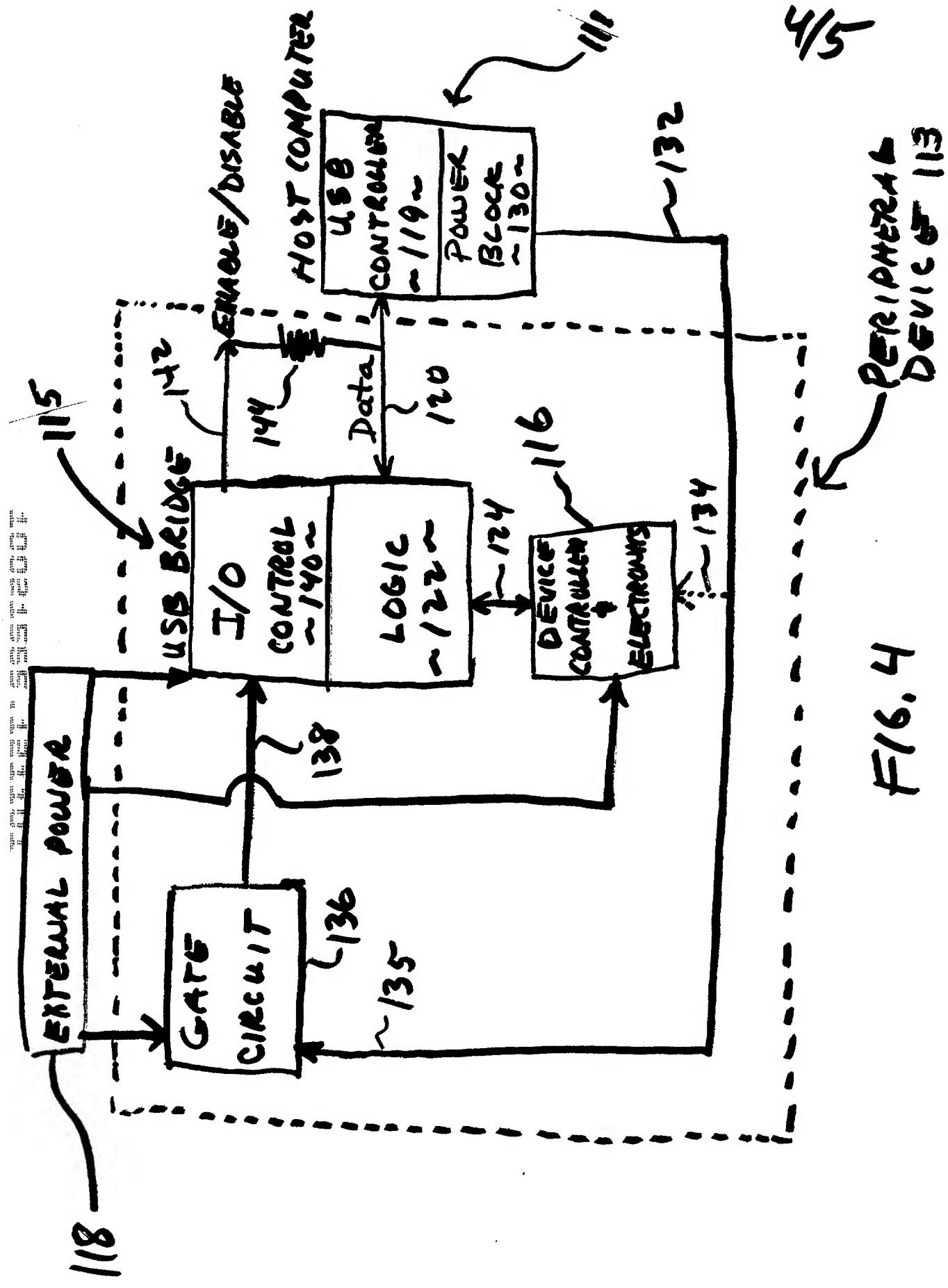


FIG. 2

FIG. 3





F16.4

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EXT Power	Bridge Logic	I/O Control	Pull-up Resistor	DATA LINE	Power Bus	Status
NO WORKING	1	ENABLE TRANSMIT	YES	/HDX	A	
YES WORKING	0	DISABLE	NON-ACTIVE	NO/HDX	B	
- DORMANT	OFF	N/A	NON-ACTIVE	-	C	
						TRI-STATE MATRIX
						151 152 153 154

FIG. 5